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IN THE UNITED STATES ELECTED/DESIGNATED OFFICE
OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
UNDER THE PATENT COOPERATION TREATY-CHAPTER II

APPLICANTS: Joachim Charzinski

DOCKET NO: 112740-386

SERIAL NO:

GROUP ART UNIT:

EXAMINER:

INTERNATIONAL APPLICATION NO: PCT/EP00/05198

INTERNATIONAL FILING DATE: 06 June 2000

INVENTION: METHOD FOR MONITORING BIT TRANSMISSION
QUALITY IN PACKET-MODE TRANSMISSION

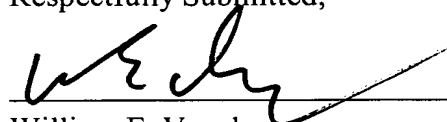
Assistant Commissioner for Patents,
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

In accordance with the provisions of 37 CFR §1.56, Applicants request that citation and examination of the references listed on Form PTO-1449, copies of which are enclosed herewith in accordance with 37 CFR §1.98, be made during the course of examination of the above-identified application for United States Patent.

Accordingly, early consideration and allowance of the application, including the claims, are hereby requested.

Respectfully Submitted,



(Reg. No. 39,056)

William E. Vaughan
Bell, Boyd & Lloyd LLC
P.O. Box 1135
Chicago, Illinois 60690-1135
(312) 807-4292
Attorneys for Applicants

37 CFR 1.501 INFORMATION DISCLOSURE STATEMENT IN A PATENT (use several sheets if necessary)						Docket No. 112740-386		Serial No.	
						Applicant: Charzinski..			
						Filing Date		Group Art Unit	
U.S. PATENT DOCUMENTS									
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate		
	AA								
	AB								
	AC								
	AD								
	AE								
	AF								
	AG								
	AH								
	AI								
	AJ								
	AK								
FOREIGN PATENT DOCUMENTS									
		Document Number	Date	Country	Class	Subclass	Translation		
							Yes	No	
	AL	0 418 130 A1	3-20-91	European					
	AM	WO 98/16037	4-16-98	PCT					
	AN								
	AO								
	AP								
	AQ								
	AR								
	AS								
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)									
	AT	XP 000692431 Improvement in Bit Error Rate for Optoelectronic Multicomputer Interconnection Networks Using Cyclic Redundancy Code Error Detection, May et al.,							
	AU	XP 000286903 Performance Monitoring of Digital Networks, Woods et al.							
	AV	XP 000793869 Instantaneous In Service Fault Location Scheme for Digital Transmission Systems, Takasaki et al.							
	AW								
Examiner				Date Considered					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.									